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PATENT

In the United States Patent and Trademark Office

Applicant: Scott T. Becker

Applicant's Ref: ARTCP012C

Application No: 10/026,245 ✓

Filed: December 17, 2001

Title: METHODS FOR REDUCING BITLINE
OFFSETS IN MEMORY DEVICES

Examiner: Ferris III, F.

Group Art Unit: 2128

Date: April 25, 2005

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on April 25, 2005.

Signed: Kay Harlow

Kay Harlow

Separate Letter to the Official Draftsperson

Commissioner for Patents
Mail Stop Issue Fee and PGPUB Drawings
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowability dated February 4, 2005, indicating that formal drawings are now required, enclosed herewith are the formal drawings (Figures 1A, 1B, 1C, 1D, 2A, 2B, 3A, 3B, 3C, and 3D), for the above-referenced case. If the draftsperson has any questions concerning these drawings, please contact the undersigned at the number set forth below. If any fees are due in connection with the filing of these drawings, please charge such fees to deposit account 50-0805 (Order No. ARTCP012C).

Respectfully submitted,

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